2SB1488

Silicon PNP triple diffusion planar type

For power switching

■ Features

- High forward current transfer ratio hFE
- High-speed switching
- ullet High collector-base voltage (Emitter open) V_{CBO}
- Allowing supply with the radial taping

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V _{CBO}	-400	V
Collector-emitter voltage (Base open)	V _{CEO}	-400	V
Emitter-base voltage (Collector open)	V_{EBO}	-7	V
Collector current	I_C	-0.5	A
Peak collector current	I_{CP}	-1	A
Collector power dissipation *	P _C	1	W
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Note) *: Print circuit board: Copper foil area of 1 cm² or more, and the board thickness of 1.7 mm for the collector portion

Unit: mm 0.65 max 1: Emitter 2: Collector 3: Base MT-2-A1 Package

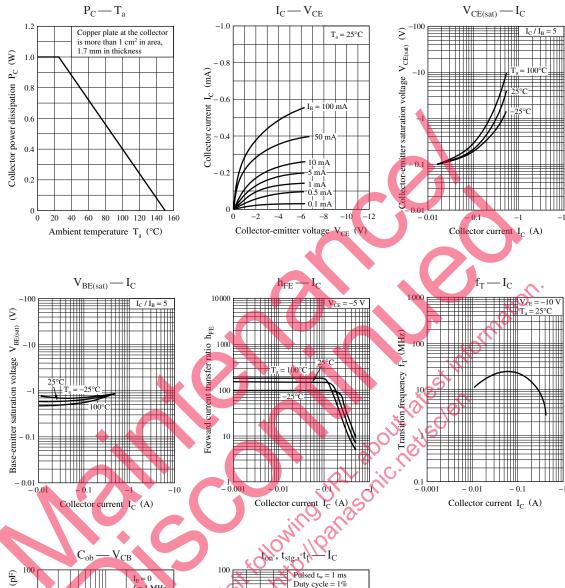
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

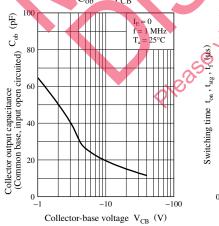
Collector power dissipation *	P _C	1	W	•	"U	1411-2-21	11 ackage	
Junction temperature	T _j	150	°C	SX.				
Storage temperature	T _{stg} -	-55 to +150	°C	762	0			
Junction temperature T _j 150 °C Storage temperature T _{stg} −55 to +150 °C Note) *: Print circuit board: Copper foil area of 1 cm² or more, and the board thickness of 1.7 mm for the collector portion ■ Electrical Characteristics T _a = 25°C ± 3°C Parameter Symbol Conditions Min Typ Max Unit								
		130	Condition	Min	T	Mari	I I mile	
Parameter Collector-emitter voltage (Base open)	Symbol	$1_{\rm C} = -1 \text{m}$	Containone	Min -400	Тур	Max	Unit V	
	V _{CEO}		<u> </u>	-400				
Collector-base cutoff current (Emitter open)	I_{CBO}		$00 \text{ V} \text{ I}_{\text{E}} = 0$			-1	μΑ	
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = -10$	00 V, I _B =0			-1	μΑ	
Emitter-base cutoff current (Collector open)	I_{EBO}	V _{ER} = -5	$V, I_C \neq 0$			-1	μΑ	
Forward current transfer ratio h _{FE1} *		$V_{CE} = -5$	$V_{\rm v}I_{\rm C} = -50 \text{ mA}$	80		280	_	
h _{FE2}	h _{FE2}	$V_{\rm CE} = -5$	$V, I_C = -300 \text{ mA}$	10				
Collector-emitter saturation voltage	VE(sat)	$I_C = -100$	mA , $I_B = -10 mA$		- 0.25	- 0.50	V	
Base-emitter saturation voltage	V _{BE(sat)}	$I_C = -100$	mA , $I_B = -10 mA$		- 0.8	-1.2	V	
Transition frequency	f_T	$V_{CB} = -10$	$V, I_E = 0.1 A, f = 200 MHz$		25		MHz	
Turn-on time	t _{on}	$I_C = -100$	mA , R_L = 1.5 $kΩ$		0.4	1.0	μs	
Storage time	t _{stg}	$I_{B1} = -10$	$mA, I_{B2} = 10 mA$		5.5	6.5	μs	
Fall time	$t_{\rm f}$	$V_{CC} = -15$	50 V		0.5	1.0	μs	
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$			20	40	pF	

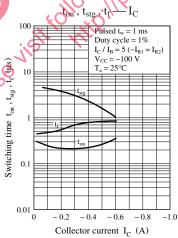
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *: Rank classification

Rank	Р	Q		
h _{FE1}	80 to 160	130 to 280		







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